Chip Inductors High Power Type

# Type: **ELJPF ELJPE**



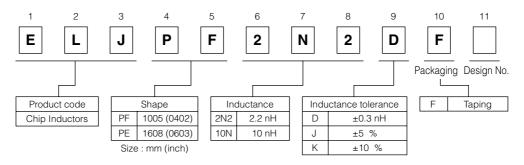
#### ■ Features

- Low DCR and large current capability, suitable for power circuitry.
- High frequency capability due to its non magnetic core.
- Capable of being Re-flow or flow soldered.
- Unique Ceramic Core / Laser-cut Technology.
- Non polarity product.
- Good for mounting.
- RoHS compliant

# ■ Recommended Applications

• Wireless communication equipment and various types of general electronic equipment.

# ■ Explanation of Part Numbers



# ■Storage Conditions

● Package : Normal temperature (–5 to 35 °C), normal humidity (85 %RH max.), shall not be exposed to

direct sunlight and harmful gases and care should be taken so as not to cause dew.

● Operating Temperature : -40 to +85 °C

# ■Storage Period

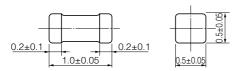
Solderability may be reduced due to the conditions of high temperature and high humidity which causes the oxidation of tin-plated terminals. Even if storage conditions are within specified limits, solderability may be reduced with the passage of time. Therefore, please control the storage conditions and try to use the product within 6 months of receipt.

# ■ Packaging Methods, Soldering Conditions and Safety Precautions

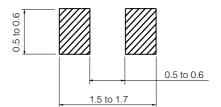
Please see Data Files.

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- PF Type 1005 (0402)
- Dimensions in mm (not to scale)



Recommended Land Pattern in mm (not to scale)

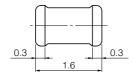


- Standard Packing Quantity
- 10000 pcs./Reel

# ■ Standard Parts

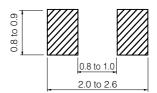
		Inductance		(	Q	SRF *1	Rpc *2	DC Current (mA) max.														
Part No.	(nH)	Tolerance (%)	Test Freq. (MHz)	min.	Test Freq. (MHz)	(MHz) min.	$(\Omega)$ max.															
ELJPF2N2DFB	2.2				5300	0.040	1900															
ELJPF2N7DFB	2.7		<u>_</u>	7	100	5300	0.050	1800														
ELJPF3N3DFB	3.3	D : ±0.3 nH				5000	0.070	1500														
ELJPF3N9DFB	3.9	D . ±0.5 III I				4800	0.080	1400														
ELJPF4N7DFB	4.7		100			4600	0.100	1300														
ELJPF5N6DFB	5.6					4200	0.120	1200														
ELJPF6N8JFB	6.8																			4000	0.160	1100
ELJPF8N2JFB	8.2	J:±5%				3700	0.210	900														
ELJPF10NJFB	10					3200	0.260	750														

- \*1 : Self Resonant Frequency \*2 : DC Resistance
- PE Type 1608 (0603)
- Dimensions in mm (not to scale)





Recommended Land Pattern in mm (not to scale)



- Standard Packing Quantity
- 3000 pcs./Reel

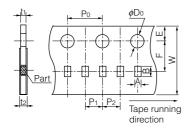
# ■ Standard Parts (E12 Series)

		Inductance		(	Q	SRF *1	Rpc *2	DC Current
Part No.	(nH)	Tolerance (%)	Test Freq. (MHz)	min.	Test Freq. (MHz)	(MHz) min.	$(\Omega)$ max.	(mA) max.
ELJPE2N2KFA	2.2			8		6000	0.030	2100
ELJPE2N7KFA	2.7			8		5500	0.030	2100
ELJPE3N3KFA	3.3			8		5500	0.040	2100
ELJPE3N9KFA	3.9			9	9 4800	5200	0.040	2100
ELJPE4N7KFA	4.7			9		4800	0.050	2100
ELJPE5N6KFA	5.6			9		4600	0.055	2100
ELJPE6N8KFA	6.8	K: ± 10 %	100	9	100	4000	0.055	1900
ELJPE8N2KFA	8.2			9		3500	0.060	1700
ELJPE10NKFA	10			9		2800	0.065	1400
ELJPE12NKFA	12			9		2500	0.080	1300
ELJPE15NKFA	15			9		2200	0.100	900
ELJPE18NKFA	18			9		2000	0.120	800
ELJPE22NKFA	22			9		1800	0.150	700

<sup>\*1 :</sup> Self Resonant Frequency \*2 : DC Resistance

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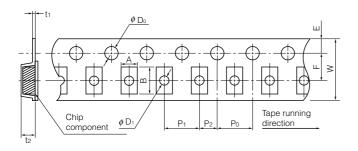
- Packaging Methods (Taping)
- Punched Carrier Tape Dimensions in mm (not to scale)



# ■ Type □F

	Α	В	W	Е	F	P <sub>1</sub>
RF, QF, PF	0.71	1.21	8.0	1.75	3.5	2.0
		_	40	,		
	P <sub>2</sub>	P∘	φD∘	Ţ1	t2	

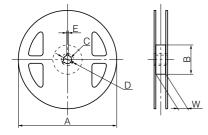
# • Embossed Carrier Tape Dimensions in mm (not to scale)



# ■ Type □E

	А	В	W	Е	F	P₁
RE, QE, PE	1.0	1.8	8.0	1.75	3.5	4.0
	$P_2$	P₀	$\phi$ D $_{0}$	$\phi$ D1	t <sub>1</sub>	t <sub>2</sub>
RE, QE, PE	2.0	4.0	<i>ϕ</i> 1.5	<i>\$</i> 0.6	(0.27)	1.2

# ● Taping Reel Dimensions in mm (not to scale)



Parts	А	В	С	D	E	W
RF, QF, PF RE, QE, PE	180	60	13	21	2	9

# ■ Standard Packing Quantity/Reel

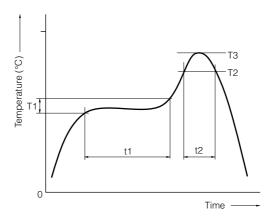
Types Quantity	Quantity
RF, QF, PF	10000 pcs.
RE, QE, PE	3000 pcs.

\* Under conditions of high temperature and humidity deterioration of the taping and packaging may be accelerated.

Please carefully control storage conditions and use the product within 6 months of receipt.

# **Soldering Conditions**

# ■ Reflow soldering conditions



# • Pb free solder recommended temperature profile

Туре	Pref	neat	Sold	ering	Peak Temperature		Time of	
	T1 [°C]	t1 [s]	T2 [°C]	t2 [s]	Т3	T3 Limit	Reflow	
□F	150 to 180	60 to 120	230 °C	40 max.	250 °C, 10 s	260 °C, 10 s	2 times max.	
	150 to 180	60 to 120	230 °C	40 max.	250 °C, 10 s	260 °C, 10 s	2 times max.	

# ■ Flow soldering conditions

Preheat: 130 to 150 °C, 60 to 180 s, Soldering: 260 °C, 5 s max.

#### ■ Notes

- Solderability may be reduced due to the conditions of high temperature and high humidity which causes the oxidation
  of tin-plated terminals. Even if storage conditions are within specified limits, solderability may be reduced with the
  passage of time. Therefore, please control the storage conditions and try to use the product within 6 months of receipt.
- In case the product has been stored for a period longer than 6 months, use the product only after confirmation of its solderability.

# 

(Common precautions for Chip Inductors)

- When using our products, no matter what sort of equipment they might be used for, be sure to make a written agreement on the specifications with us in advance. The design and specifications in this catalog are subject to change without prior notice.
- Do not use the products beyond the specifications described in this catalog.
- This catalog explains the quality and performance of the products as individual components. Before use, check and evaluate their operations when installed in your products.
- Install the following systems for a failsafe design to ensure safety if these products are to be used in equipment where a defect in these products may cause the loss of human life or other significant damage, such as damage to vehicles (automobile, train, vessel), traffic lights, medical equipment, aerospace equipment, electric heating appliances, combustion/gas equipment, rotating equipment, and disaster/crime prevention equipment.
- \* Systems equipped with a protection circuit and a protection device
- \* Systems equipped with a redundant circuit or other system to prevent an unsafe status in the event of a single fault

#### ⚠ Precautions for use

#### 1. Operation range and environments

- (1) These products are designed and manufactured for general and standard use in general electronic equipment (e.g. AV equipment, home electric appliances, office equipment, information and communication equipment)
- ② These products are not intended for use in the following special conditions. Before using the products, carefully check the effects on their quality and performance, and determine whether or not they can be used.
  - In liquid, such as water, oil, chemicals, or organic solvent
  - In direct sunlight, outdoors, or in dust
  - In salty air or air with a high concentration of corrosive gas, such as Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, or NO<sub>2</sub>
  - In an environment where these products cause dew condensation

#### 2. Handling

- ① Do not bring magnets or magnetized materials close to the product. The influence of their magnetic field can change the inductance value.
- ② Do not apply strong mechanical shocks by either dropping or collision with other parts. Excessive schock can damage the part.

#### 3. Land pattern design

- ① Please refer to the recommended land pattern for each type shown on the datasheet.
- ② Avoid placing the chip inductor on any metal pattern except the recommended land pattern because a drop of Q and mutual conductance may occur.
- ③ In case of flow soldering, venting of soldering flux gases should be made for high density assemblies to get a good solder connection.
- ④ In case of reflow soldering, consider the layout because taller components close to chip inductor tend to block thermal conduction.

#### 4. Mounting

- ① In general, magnetic and electric characteristics of ferrite cores can be changed by applying excessively strong force. Placement force should not exceed 20 N.
- 2 Do not bend or twist the PWB after mounting the part.

#### 5. Cleaning

- ① Do not use acid or alkali agents. Some cleaning solvents may damage the part.
  - Confirm by testing the reliability in advance of mass production.
- ② If Ultrasonic cleaning is used, please confirm the reliability in advance. It is possible that combined resonance of component and PWB and cavitation can cause an abnormal vibration mode to exist causing damage.

#### 6. Caution about applying excessive current

The rated current is defined as the smaller value of either the current value when the inductance drops 10 % down from the initial point or the current value when the average temperature of coil inside rises 20 °C up from the initial point. Do not operate product over the specific max. current.

#### <Package markings>

Package markings include the product number, quantity, and country of origin. In principle, the country of origin should be indicated in English.